

Serial No. Not assigned

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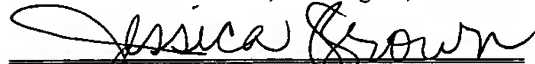
10/031672 #2


17 JAN 2002

Docket No. 1803624199

PATENT

I hereby certify that on January 17, 2002, which is the date I am signing this certificate, I am depositing this correspondence the United States Postal Service, first class mail, in an envelope addressed to the Commissioner of Patents, Washington, D.C. 20231

  
Jessica Brown

Applicant: Athas, William S et al.   
Serial No.: Filed herewith  
Filed: Herewith  
Title: High Performance Clock-Powered Logic  
Examiner: Not assigned  
Group Art Unit: Not known

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Commissioner for Patents  
Washington, D.C. 20231

### INFORMATION DISCLOSURE STATEMENT

Sir:

Applicant brings the references listed below and on the attached Form PTO-1449 to the examiner's attention. 37 C.F.R. § 1.56. Do not construe the filing of this information disclosure statement as a representation that applicant has made a search (37 C.F.R. § 1.97(g)), or as an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists. We enclose copies of the cited documents.

We bring these foreign language documents to the examiner's attention with a concise explanation of the relevance of each document, as the individual designated in 37 C.F.R. § 1.56(c) as most knowledgeable about the content of the information of each document presently understands it.

We request that the examiner show that he or she considered the cited references by returning a copy of the attached form PTO-1449 with initials or other appropriate marks, and that the examiner make the references of record as cited references in the application.

January 17, 2001

  
Marc E. Brown, Reg. No. 28,590  
Attorney for Applicant

**Oppenheimer Wolff & Donnelly LLP**  
2029 Century Park East, 38<sup>th</sup> Floor  
Los Angeles, CA 90067-3024  
Telephone: (310) 788-5000 • Fax: (310) 788-5100